

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "(burgoon d. a.&lt;in&gt;au)"

Your search matched 1 of 1237766 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

e-mail

## » Search Options

[View Session History](#)[New Search](#)

## Modify Search

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **A mixed-language simulator for concurrent engineering**  
Burgoon, D.A.;  
Verilog HDL Conference and VHDL International Users Forum, 1998. IVC/VIUI  
1998 International  
16-19 March 1998 Page(s):114 - 119  
Digital Object Identifier 10.1109/IVC.1998.660689  
[AbstractPlus](#) | Full Text: [PDF\(100 KB\)](#) IEEE CNF

Indexed by  
 Inspec[Help](#) [Contact Us](#) [Privacy &](#)

© Copyright 2005 IEEE -